

**Amendments to the Specification**

Please amend the title of the application at page 1 as follows:

~~System-on-Chip~~ Breakpoint ~~Synchronization~~ Handling

Please replace the paragraph beginning at page 1, line 13 with the following amended paragraph:

Isolating a hardware or software defect within the SOC often requires using a debugging tool to set a breakpoint condition for a processor in the SOC. Once the condition is satisfied, the processor and the peripherals are stopped and the breakpoint handler is executed. After stepping through the breakpoint handler to isolate the problem, the processors and peripherals are restarted. However, the internal architecture of a typical SOC lacks the ability to effectively stop and restart multiple independent processors and peripherals without ~~the experiencing loose~~ loss of synchronization among the components in the SOC.